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U.S. Patent Application No. 10/735,364, filed December 12, 2003, Docket No. P05762 (11461.00.5762)

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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Application Number 10/735,384 Filing Date **POWER OF ATTORNEY** December 12, 2003 First Named Inventor Varadarajan Devnath and Title Transimpedance amplifier with controllable noise reduction CORRESPONDENCE ADDRESS Art Unit 2817 **INDICATION FORM** Examiner Name Steven J. Mottola

	Attorney Docker Number	P05782 (114B1.00.5762)
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STATEMENT UNDER 37 CFR 3.73(b)		
Applicant/Patent Owner: Varadarajan Devnath		
Application No./Patent No.: 10/735,364 Filed/Issue Date: December 12, 2003		
Entitled: Transimpedance amplifier with controllable noise reduction		
National Semiconductor Corporation , 3 (Name of Assignoe) , Corporation (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)		
states that it is: 1.		
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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee. $\mathcal{U} = \mathcal{U} = \mathcal{U} = \mathcal{U}$		
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Christopher J. Byrne 408-721-6155		
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